

**REMARKS**

Applicant thanks the Examiner for acknowledging the claims to priority under 35 U.S.C. § 119 and acknowledging receipt of the priority document.

Applicant also thanks the Examiner for considering the references submitted with the Information Disclosure Statements filed on January 2, 2004 and March 7, 2006.

The Examiner's acceptance of the drawings filed July 7, 2003, is also appreciated.

Claims 1-22 have been examined. These claims have been rejected under 35 U.S.C. § 102(b) as allegedly anticipated by Williams et al. (EP0817053A1), hereinafter Williams. This rejection is respectfully traversed. For the following reasons it is submitted that the claims of the application patentably distinguish over Williams.

Claim 1 is independent. Claims 2-4, 9-11 and 23 depend from claim 1. Claim 12 is an independent claim, with claims 13-15, 20-22 and 24 depending from claim 12. Claims 5-8 and 16-19 have been canceled without disclaimer or prejudice to the filing of the subject matter of these claims in a subsequent application. For the reasons that follow it is clear that independent claims 1 and 12 along with their respective dependent claims patentably distinguish over the Williams reference.

A feature of independent claims 1 and 12 resides in a third memory element that stores an address accessing a data word directed by a write access request at the time that the monitor element finds that the first computer element is out of synchronism.

In the present invention the address stored in the third memory element is directed by a write access request at the time when the monitor element finds that the second computer is out

of synchronism to write a data word. Therefore, with the third memory element of the present invention it is possible to record an update of memory after the out of synchronism event by a complete data word. That is, the unit of update is a data word.

On the contrary, Williams discloses that “the recording of memory updates (writes) is not based on recording each address accessed but rather on memory segments (pages) updated (written to)”. Williams also specifies “the first and/or second recording mechanisms record the segments (or pages) that are updated (written to)” (see column 3, lines 5-13). Therefore, a dirty RAM storage 46 does not record an update in data word units. Williams clearly concedes this. Therefore, Williams neither teaches nor suggests a third memory as is set forth in independent claims 1 and 12 nor does Williams suggest the combination of elements set forth therein.

It is therefore respectfully submitted that independent claims 1 and 12 and their respective dependent claims patentably distinguish over Williams and are otherwise in condition for allowance.

It is therefore respectfully requested that the application be passed to issue at the earliest possible time. If for any reason the application is found other than to be in condition for allowance, the Examiner is respectfully requested to call the undersigned attorney at the Washington, D.C. telephone number (202) 293-7060 to discuss the steps necessary for placing the application in condition for allowance.

Amendment Under 37 C.F.R. § 1.111  
Application No. 10/612,930

The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

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